

FORM PTO 1449 (Modified) MAIL ROOM JUL 12 1995 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		Attorney Docket No. 12172-005010		Serial No.: 08/422,753		
Applicant: Howard G. Sachs, et al.						
Filing Date: 04/13/95				Group: 2315		
Reference Designation U.S. PATENT DOCUMENTS						
Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)
___ AA						
___ AB						
___ AC						
___ AD						
___ AE						
___ AF						
___ AG						
___ AH						
___ AI						
___ AJ						
___ AK						
___ AL						
FOREIGN PATENT DOCUMENTS						
	Document No.	Date	Country	Class	Sub-class	Translation (yes/no)
<u>UAD</u> AM ✓	Publ. 0 449 661 A2 Appl. 91302832.0	10/02/91	EPO	—	—	
<u>UAD</u> AN ✓	Publ. 0 496 928 A2 Appl. 91105248.8	08/05/92	EPO	—	—	
___ AO						
___ AP						
___ AQ						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<u>UAD</u> AR ✓	Minagawa, Kenji, et al., <i>IEEE Pacific Rim Conference on Communications, Computers and Signal Processing</i> , May 9-10, 1991, "Pre-decoding mechanism for superscalar architecture", p. 21-24.					
<u>UAD</u> AS ✓	De Gloria, Alessandro, et al., <i>Proceesings Comp. Euro. 1992</i> , May 4, 1992, "A programmable instruction format extension to VLIW architectures", p. 35-40.					
___ AT						
EXAMINER <u>Valerie Darte</u> DATE CONSIDERED <u>March 24, 1996</u>						

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		Applicant: H. Sachs et al.				
		Filing Date: 04/13/95		Group: (unassigned)		
Reference Designation U.S. PATENT DOCUMENTS						
Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)
<u>1A0</u> AA	4,437,149	03/13/84	Pomerene et al.	364	200	1/80
<u>1A0</u> AB	4,933,837	06/12/90	Freidin	364	200	6/90
<u>1A0</u> AC	5,055,997	10/08/91	Sluijter et al.	364	200	12/88
<u>1A0</u> AD	5,081,575	01/14/92	Hiller et al.	395	325	11/87
<u>1A0</u> AE	5,101,341	03/31/92	Circello et al.	395	375	8/89
<u>1A0</u> AF	5,121,502	06/09/92	Rau et al.	395	8	12/89
<u>1A0</u> AG	5,129,067	07/07/92	Johnson	395	375	6/89
<u>1A0</u> AH	5,151,981	09/29/92	Wescott et al.	395	375	7/90
<u>1A0</u> AI	5,179,680	01/12/93	Colwell et al.	395	425	4/87
<u>1A0</u> AJ	5,214,763	05/25/93	Blaner et al.	395	375	5/90
<u>1A0</u> AK	5,226,169	07/06/93	Gregor	395	800	12/88
<u>1A0</u> AL	5,233,696	08/03/93	Suzuki	395	375	8/90
<u>1A0</u> AM	5,297,255	03/22/94	Hamanaka et al.	395	200	1/88
FOREIGN PATENT DOCUMENTS						
	Document No.	Date	Country	Class	Sub-class	Translation (yes/no)
___ AN						
___ AO						
___ AP						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<u>1A0</u> AQ	Case et al., "DEC enters microprocessor business with alpha," <i>Microprocessor Report</i> 6(3):1,6-14 (March 4, 1992).					
<u>1A0</u> AR	Dutton, "The design of the DEC 3000 model 500 AXP workstation," <i>IEEE</i> 1063-6390/93, pp. 449-455 (1993).					
<u>1A0</u> AS	Allison, "DEC 7000/10000 model 600 AXP multiprocessor server," <i>IEEE</i> 1063-6390/93, pp. 456-464 (1993).					
<u>1A0</u> AT	Grove et al., "GEM optimizing compilers for alpha AXP systems," <i>IEEE</i> 1063-6390/93, pp. 465-473 (1993).					
EXAMINER <u>Valerie Darte</u> DATE CONSIDERED <u>March 28, 1996</u>						

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